

CLOCK OSCILLATORS

T3000LP Series Clock Oscillator

Low Stand-by Current, SMD



T3000LP

RoHS Compliant Standard

Features:

- ◆ Low Stand-By Current
- ◆ Ceramic SMD package
- ◆ Tape and Reel Available
- ◆ Compatible with pick and place
- ◆ HCMOS/ TTL compatible

Model: T3000LP Series Specifications

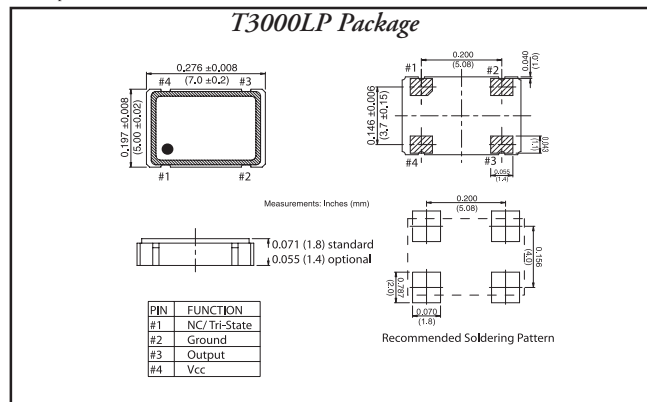
Parameter	Symbol	Unit	Min.	Max.
Center Frequency	f_o	MHz	8.5	125
Operating Temperature Range	T	°C	See Table	
Frequency Stability (Note: 1)		ppm	See Table	
Voltage		V	1.8, 2.5, 3.3, 5.0	
Supply Current	I_{DD}	mA		
1MHz < 19MHz		mA	10	7
20MHz < 49MHz		mA	30	20
50MHz < 69MHz		mA	40	30
70MHz < 125MHz		mA	40	30
Output Levels (CMOS)				
Output High (Logic "1")	V_{OH}	V	90%	
Output Low (Logic "0")	V_{OL}	V		10%
Transition Times (Note: 2)	T_R/T_F			
Rise/ Fall Time (1MHz to 19MHz)		ns	8	10
Rise/ Fall Time (20MHz to 49MHz)		ns	5	6
Rise/ Fall Time (50MHz to 69MHz)		ns	2	3
Rise/ Fall Time (70MHz to 125 MHz)		ns	2	3
Start Up Time		mSec		8
Output Symmetry	SYM	%	40	45 / 55 60
Absolute Clock Period Jitter		pSec		40
Storage Temperature		°C	-55	+125
Standby Current		uA		10
Tri-State (input to pin 1)				
Output Active	Tri-State	V	4.0 2.0 1.75 1.26	
Output in High-Impedance State	Tri-State	V		0.8 0.5

(1) Inclusive of calibration at 25°C, operating temperature range, input voltage variation, load variation, aging, shock and vibration.

(2) Transition times are measured between 10% and 90% of VDD, with an output load of 15pF

(3) 50pF output load available

Temperature	Stability (ppm)
0 to 50°C	±20, ±25, ±32, ±50, ±100
-10 to 60°C	±20, ±25, ±32, ±50, ±100
-20 to 70°C	±20, ±25, ±32, ±50, ±100
-40 to 85°C	±25, ±32, ±50, ±100



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