



T5000LP

CLOCK OSCILLATORS

T5000LP Series Clock Oscillator

Low Voltage, Low Profile, SMD



Features:

- ◆ Low Current Draw
- ◆ HCMOS Output
- ◆ Ceramic SMD package
- ◆ Tape and Reel Available

Model: T5000LP Series Specifications

Parameter	Unit	T5000LP
Frequency Range (1)	MHz	1.00 to 60.00
Operating Temperature Range	°C	See Table
Frequency Stability (2)	ppm	See Table
Supply Voltage	VDC	0.9, 1.8, 2.5, 3.3
Supply Current	mA	10 max.
WaveForm		CMOS
Load	pF	15
Output Levels	V	10% max. // 90% min.
Transition Times (Rise/Fall)	nSec	10 max.
Duty Cycle	%	40/60%
Aging (per year)	ppm	±5.0 max.
Storage Temperature	°C	-55 to 125
Tri-State		Pin 1
Enable	V	0.7 min.
Disable	V	0.3 max.
Standby Current	mA	10
Start Up Time	ms	10
Period Jitter; (12kHz to 20MHz)	ps	1 max.
Period Jitter: pk-pk	ps	±100 max
Period Jitter: One Sigma	ps	±25 max.

(1) Inclusive of calibration @ 25°C, operating temperature, Input voltage change, load change, shock, and vibration.

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Temperature	Stability (ppm)
0 to 70°C	±20, ±25, ±50
-10 to 70°C	±20, ±25, ±50
-20 to 70°C	±20, ±25, ±50
-40 to 85°C	±25, ±50

