



VCXOs V3000TLP-A Series Ceramic, SMD



VOLTAGE CONTROLLED CRYSTAL OSCILLATOR: V3000TLP-A Series

Features:

- ❖ LVPECL
- ❖ 7.0 x 5.0 x 1.85 mm
- ❖ Low Jitter of 0.1 RMS
- ❖ Up to 800 MHz

| Parameter | Unit | Min. | Max. |
|---------------------------------|--------|-------------------|--------|
| Frequency Range | MHz | 250.00 | 800.00 |
| Frequency Stability | | | |
| -10 to +60 °C | ppm | ±25 | ±50 |
| -20 to +70 °C | ppm | ±25 | ±50 |
| -40 to +85 °C | ppm | ±50 | ±100 |
| Storage Temperature Range | °C | -55 | +125 |
| Aging (per year) | ppm | - | ±3.0 |
| Voltage | V | 3.3 ±5% | |
| Current Consumption | | | |
| 250.00 to 800.00 MHz | mA | - | 100 |
| Output Waveform | | LVPECL | |
| Output Load | pF | - | 15 |
| Output Voltage Logic High (VOH) | V | 90% | - |
| Output Voltage Logic Low (VOL) | V | - | 10% |
| Transition Time (Rise and Fall) | nSec | - | 1.0 |
| Duty Cycle | | 40/60% standard | |
| Tristate | | | |
| Enable | V | 0.7 | - |
| Disable | V | - | 0.3 |
| Frequency Deviation | ppm | ±50 min. standard | |
| Control Voltage | V | 1.65 ±1.5 | |
| Start-up Time | mSec | - | 3 |
| Modulation Bandwidth | kHz | 40 | - |
| Input Impedance | KOHms | 130 | - |
| RMS Jitter (12 kHz to 20 MHz) | pSec | - | 0.1 |
| RMS Jitter (50 kHz to 80 MHz) | pSec | - | 0.15 |
| Phase Noise | | | |
| @ 100 Hz | dBc/Hz | - | -70 |
| @ 1 kHz | dBc/Hz | - | -100 |
| @ 10 kHz | dBc/Hz | - | -120 |

Frequency Stability is inclusive of Operating Temperature Range, Supply Voltage, Current and Load.

V3000TLP-A Package

Measurements: Inches (mm)
All Tolerances: ±0.008 (±0.2)

Recommended Solder Pattern

| Environmental | | | |
|-------------------|--|-------|--|
| Terminal Material | | W | |
| Terminal Plating | | Ni/Au | |
| REACH Compliant | | Yes | |
| RoHS Compliant | | Yes | |
| RoHS Exemption | | No | |
| Reflow Temp. Max. | | 260°C | |
| MSL | | 1 | |

| PAD | Function | PAD | Function |
|-----|-----------|-----|-----------------|
| 1 | Vcontrol | 4 | Output |
| 2 | Tri-state | 5 | Comp. Output |
| 3 | GND | 6 | V _{DD} |